

Silicon Carbide Schottky Diode

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■ Features

- Very low profile-typical height of 1.7 mm
- · High surge current capability
- No reverse recovery
- Positive Temperature Coefficient
- Easy to paralleling
- Halogen-free / RoHS compliant
- Compliance with EU REACH

■ Benefits

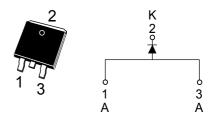
- High-speed switching
- Low heat dissipation requirements
- Reduce size and cost of the system
- High-reliability
- System efficiency improvement

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- Solar inverter
- Power factor correction
- Data Center
- Switch mode power supply

V_{RRM}	650V	
l _F	20A(Tc=154°C)	
Q _C	41nC	





Package:TO-263M-2L ECR2065AM-HF HF=Halogen Free

Absolute Maximum Ratings (Tc=25°C)

Symbol	Parameter		Data	Unit
V_{RRM}	Repetitisve Peak Reverse Voltage		650	V
I _F	Continuous Forward Current	Tc=135°C	30	Α
		Tc=154°C	20	Α
I _{FSM}	Non-Repetitive Forward Surge Current	Tc=25°C,Tp=8.3mS,Half Sine Pulse	160	А
P _{tot}	Power Dissipation	Tc=25°C	214	W
T _J	Operating Junction Temperature		-55∼175	°C
T _{STG}	Storage Temperature		-55∼175	°C
$R_{ heta JC}$	Thermal Resistance Junction to Case (per leg)		TYP:0.7	°C/W



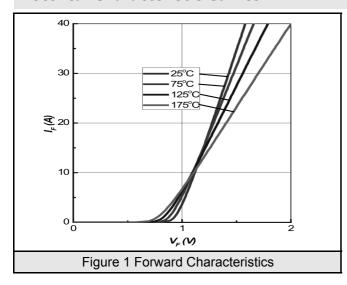


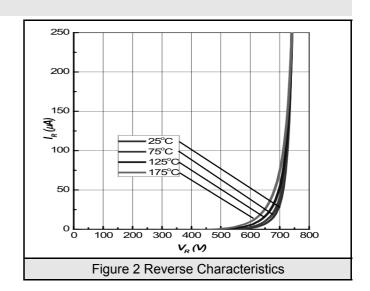
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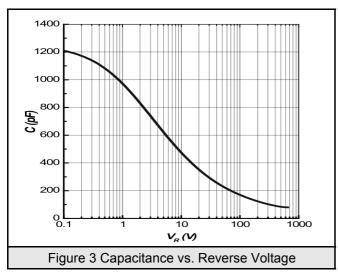
Electricity Character Per Diode (Tc=25°C)

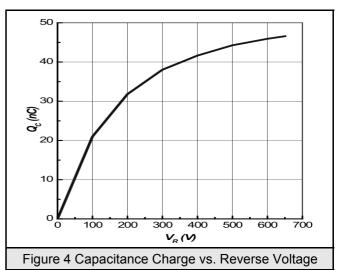
Item	Test Co	ondition	Value(min)	Value(typ)	Value(max)	Unit
V_{B}	_	Tc=25°C	650	_	_	V
V _F	IF=20A	Tc=25°C	_	1.3	1.5	V
		Tc=175°C	_	1.4	_	V
I _R	VR=650V	Tc=25°C	_	10	100	uA
		Tc=175°C	_	40	_	uA
С	f=1MHZ	VR=1V	_	980	_	pF
		VR=200V	_	124	_	pF
		VR=400V	_	90	_	pF
Q _C	VR=400V,IF=20	A,di/dt=200A/µS	_	41	_	nC

Electrical Characteristic Curves









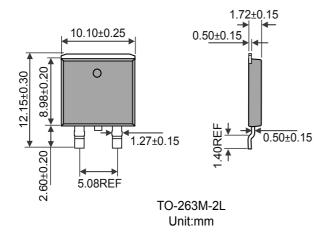


ECR2065AM

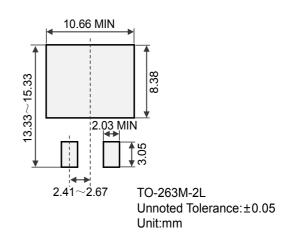


Package Outline Dimensions & Suggested Solder Pad Layout

Package Outline Dimensions



Suggested Solder Pad Layout



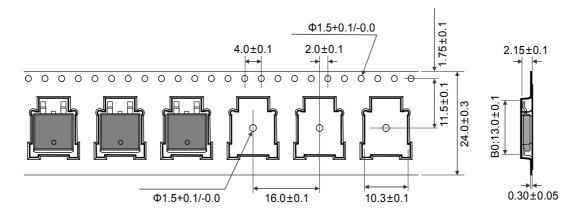
Marking Information



- "MHCHXM"= Product Logo
- "Marking Code"= The Following
- "XXXX"= Date Code Marking

Marking Code	Part Number	
ECR2065AM	ECR2065AM-HF	

Reel Tape Dimensions





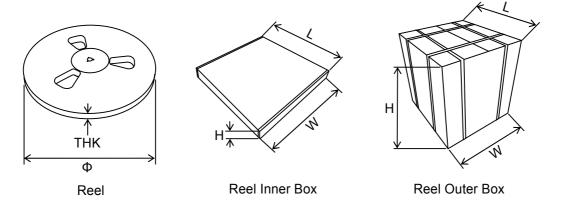
ECR2065AM

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Packing Information

Packaging	Part Number	Quantity(pcs)	Size(mm)
	Reel	1500 Ф330×ТНК25	
Reel	Inner Box	1500	L355×W335×H35
	Outer Box	15000	L385×W365×H365

Packaging:Reel



Notes

Lead Forming

1.During lead frame bending, the lead frame should be bent at a distance more than 3mm from bottom of the epoxy.And the bending degree should not exceed 90°.

Note: The lead frame must be secured and do not touch the epoxy before bending to avoid damage to the transistor. In addition, when using a mold for a large number of lead molding, the structure of the fixed lead must be set, and it should be noted that the lead pressure rod structure cannot exert pressure on the epoxy resin body.

2. Do not bend the lead repeatedly. Do not bend the lead outward







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Heat sink mounting

For power devices, in order to reduce junction temperature, heat dissipation blocks are usually used to disperse heat to the outside, and semiconductor power devices installed on the heat dissipation blocks can effectively dissipate heat without losing the reliability of the semiconductor, so the following matters should be noted when using:

1. Pay attention to the selection of silicone cream

In order to improve the thermal conductivity and heat dissipation effect of the device and the heat dissipation block, generally apply a thin layer of silicone grease evenly on the contact surface of the device and the heat dissipation block. Choose a silicone grease with low oil separation degree. Do not overapply it, otherwise it will attach too much stress to the resin.

2. Optimum torque is required

When using the fastening torque, pay attention not to use too much torque, so as not to damage the epoxy resin body, pay attention to the smooth cooling block body, no file chips and other foreign bodies between the transistor and the cooling block, pay attention to the selection of screws, nuts, gaskets and washers, so as not to cause damage to the transistor due to improper selection.

Soldering

- 1. Pay special attention to welding. When welding, the distance between the solder joint and the epoxy ball should be greater than 3mm, and it is recommended to weld it outside the tie rod base.
- 2. Avoid applying any pressure to the lead frame while the transistor is at high temperatures, especially when welding. Dip welding and manual welding should not be done more than once

Notes:

For specific precautions, please refer to our company's relevant technical documents or visit our official website at http://www.jshxm.com





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