

Schottky Barrier Rectifiers

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■ Productor Character

- Multilayer Metal -Silicon Potential Structure.
- Low Leakage Current.
- High Current Capability, High Efficiency.
- High Junction Temperature Capability.
- RoHs Product.

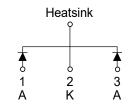
■ Summarize

• EBR30LH200CT Device optimized for ultra-low forward voltage drop to maximize efficiency in Power Supply applications.

■ Primary Use

- Low Voltage High Frequency Switching Power Supply.
- Low Voltage High Frequency Invers Circuit.
- Low Voltage Continued Circuit and Protection Circuit.

■ Equivalent Circuit







Package:TO-220AB EBR30LH200CT EBR30LH200CT-HF HF=Halogen Free

Absolute Maximum Ratings

Item	Symbol	Data	Unit	
Maximal Inverted Repetitive Peak Voltage		V_{RRM}	200	V
Average Rectified Forward Current(Rated VR-	Per Device	I _{FAV}	30	А
20Khz Square Wave)-50% duty cycle	Per Diode		15	
Forward Peak Surge Current(Rated Load 8.3 Hamsine Wave-According to JEDEC Method)	Per Diode	I _{FSM}	300	А
Operating Junction Temperature		T _J	- 40∼175	°C
Storage Temperature		T _{STG}	- 40~175	°C
Typical Thermal Resistance(per leg) Package	=TO-220AB	$R_{ heta JC}$	2	°C/W

Electricity Character Per Diode(TJ=25°C unless otherwise specified)

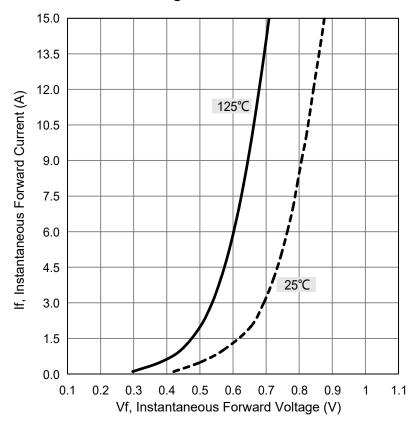
Heatsink

Item	Test Co	ondition	Value(min)	Value(typ)	Value(max)	Unit
V _B	TJ =25℃	IR=1mA	200			V
	TJ =25℃	VR=200V	_		0.01	mA
I _R	TJ =125℃	VR=200V	_	_	3.00	mA
V	TJ =25℃	IF=15A	_	0.88	0.93	V
V_{F}	TJ =125°C	IF=15A	_	_	0.8	V

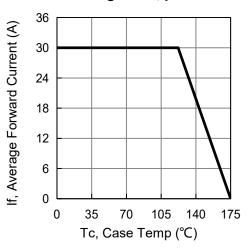


Characteristic Curves

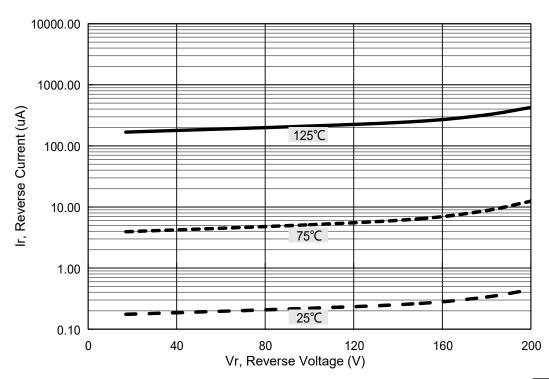
The forward voltage and forward current curve



Current derating curve, perelement



The reverse leak current and the reverse voltage (single-device) curve.



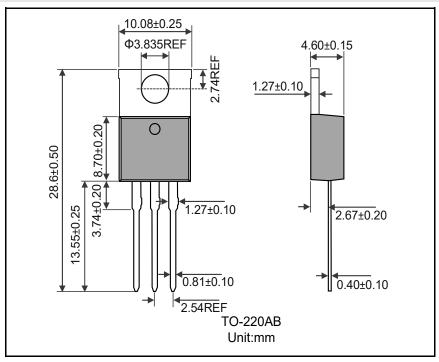


REV-F(1)



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Package Outline Dimensions



Marking Information



- "MHCHXM"= Product Logo
- "Marking Code"= The Following
- "XXXX"= Date Code Marking

Marking Code	Part Number		
EBR30LH200	EBR30LH200CT		
	EBR30LH200CT-HF		

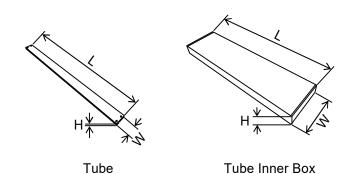


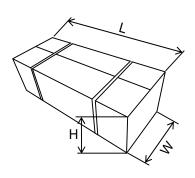


Packing Information

Packaging	Part Number	Quantity(pcs)	Size(mm)
	Tube	50	L534×W33×H7
Tube	Inner Box	1000	L560×W150×H40
	Outer Box	5000	L570×W235×H175

Packaging:Tube





Tube Outer Box

Notes

Lead Forming

1.During lead frame bending, the lead frame should be bent at a distance more than 3mm from bottom of the epoxy.

Note: The lead frame must be secured and do not touch the epoxy before bending to avoid damage to the transistor. In addition, when using a mold for a large number of lead molding, the structure of the fixed lead must be set, and it should be noted that the lead pressure rod structure cannot exert pressure on the epoxy resin body.

2. Do not bend the lead repeatedly. Do not bend the lead outward



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Heat sink mounting

For power devices, in order to reduce junction temperature, heat dissipation blocks are usually used to disperse heat to the outside, and semiconductor power devices installed on the heat dissipation blocks can effectively dissipate heat without losing the reliability of the semiconductor, so the following matters should be noted when using:

1. Pay attention to the selection of silicone cream In order to improve the thermal conductivity and heat dissipation effect of the device and the heat dissipation block, generally apply a thin layer of silicone grease evenly on the contact surface of the device and the heat dissipation block. Choose a silicone grease with low oil separation degree. Do not overapply

2. Optimum torque is required

it, otherwise it will attach too much stress to the resin.

When using the fastening torque, pay attention not to use too much torque, so as not to damage the epoxy resin body, pay attention to the smooth cooling block body, no file chips and other foreign bodies between the transistor and the cooling block, pay attention to the selection of screws, nuts, gaskets and washers, so as not to cause damage to the transistor due to improper selection.

Soldering

- 1. Pay special attention to welding. When welding, the distance between the solder joint and the epoxy ball should be greater than 3mm, and it is recommended to weld it outside the tie rod base.
- 2. Avoid applying any pressure to the lead frame while the transistor is at high temperatures, especially when welding. Dip welding and manual welding should not be done more than once

Notes:

For specific precautions, please refer to our company's relevant technical documents or visit our official website at http://www.jshxm.com



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